# Vishay Siliconix

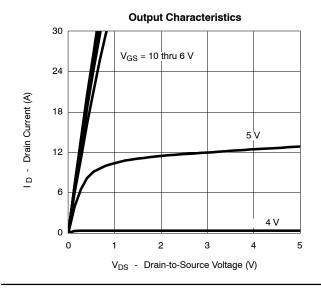


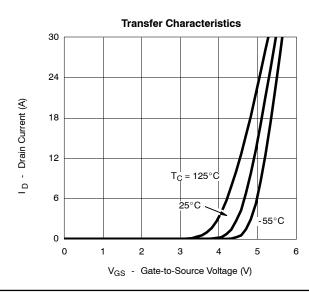
SPECIFICATIONS (T <sub>J</sub> = $25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static			•	•	•	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.0			- v
Linear Threshold Voltage	V <sub>T</sub>			4.4		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = $\pm 20$ V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V			1	μΑ
		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 7.7 A		0.021	0.025	Ω
		$V_{GS} = 6.0 \ V, I_D = 6.9 A$		0.025	0.031	
Forward Transconductancea	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_D = 7.7 \text{ A}$		23		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_S = 2.6 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.2	V
Dynamic <sup>b</sup>			•	•	'	•
Total Gate Charge	Qg	$V_{DS} = 50 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_D = 7.7 \text{ A}$		29	36	nC
Gate-Source Charge	Q <sub>gs</sub>			9.9		
Gate-Drain Charge	Q <sub>gd</sub>			10.3		
Gate Resistance	R <sub>G</sub>		0.5	1.2	1.8	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DD} = 50 \text{ V},  R_L = 50  \Omega$ $I_D \cong 1.0  A,  V_{GEN} = 10  V,  R_G = 6  \Omega$		17	26	ns
Rise Time	t <sub>r</sub>			13	20	
Turn-Off Delay Time	t <sub>d(off)</sub>			36	54	
Fall Time	t <sub>f</sub>			26	40	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 2.6 A, di/dt = 100 A/μs		45	68	1

#### Notes

- Pulse test; pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ . Guaranteed by design, not subject to production testing.

## TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

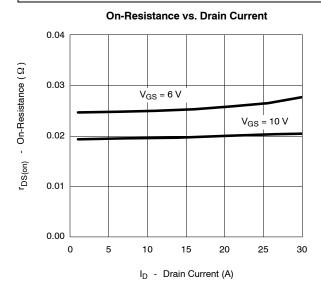




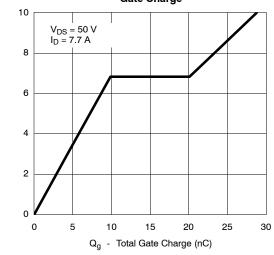




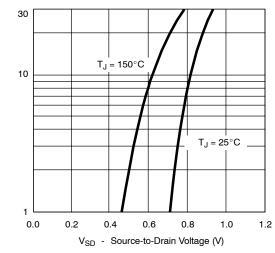
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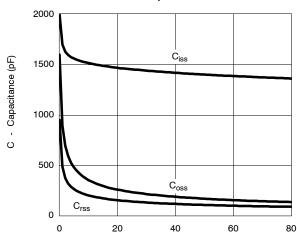




#### Source-Drain Diode Forward Voltage

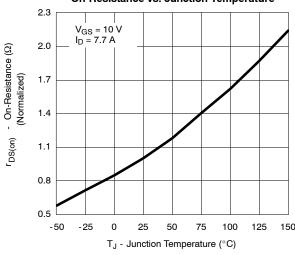


#### Capacitance

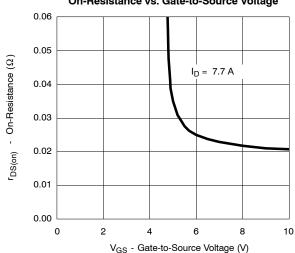


V<sub>DS</sub> - Drain-to-Source Voltage (V)

#### On-Resistance vs. Junction Temperature



## On-Resistance vs. Gate-to-Source Voltage



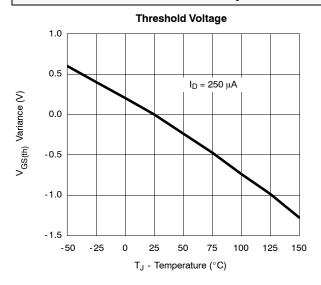
- Source Current (A)

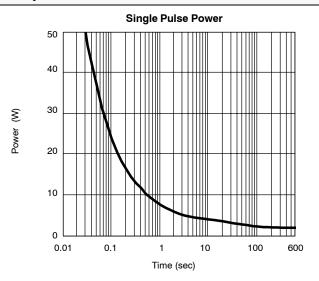
V<sub>GS</sub> - Gate-to-Source Voltage (V)

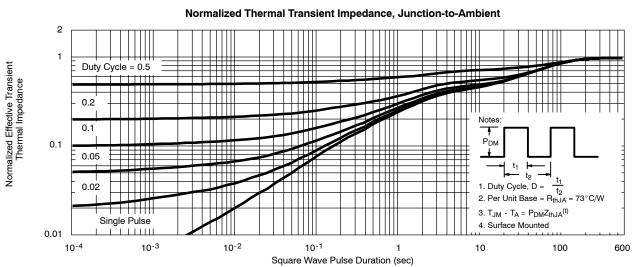
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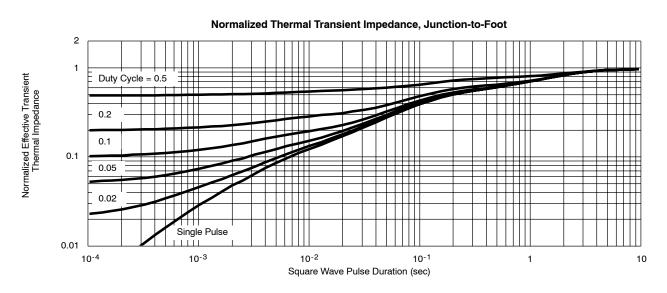


## TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)











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