Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential	–0.5V to +4.6V
DC Input Voltage	–0.5V to +4.6V
DC Output Current	120mA
Power Dissipation	0.5W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not mplied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 3.3V ±10%)

Parameters	Description	Test Conditions[1]	Min.	Typ.[2]	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}			±1	µA
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND			±1	
I_{OZH}	High Impedance Output Current	0 ≤ In, Yn ≤ V_{CC}			±1	
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = –18mA			–1.2	V
R_{ON}	Switch On-Resistance[3]	V_{CC} = Min., V_{IN} = 0.0V, Ion = 48mA or 64mA		5	8	Ω
		V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA		8	17	

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 3.3V, T_A = 25°C ambient and maximum loading.
3. Measured by the voltage drop between I and Y pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I,Y) pins.

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters[1]	Description	Test Conditions	Typ.	Units
C_{IN}	Input Capacitance	V_{IN} = 0V	3.0	pF
C_{OFFYN}	Y_N Capacitance, Switch OFF		17.0	
C_{OFFIN}	I_N Capacitance, Switch OFF		8.5	
C_{ON}	I_N/Y_N Capacitance, Switch ON		25	

Notes:
1. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions[1]		Min.	Typ.[2]	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max.	V_{IN} = GND or V_{CC}		0.1	3.0	µA
ΔI_{CC}	Supply Current per Input @ TTL HIGH[3, 4]	V_{CC} = Max.	V_{IN} = 3.0			750	

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 3.3V, +25°C ambient.
3. Per TTL driven input (control inputs only); I and Y pins do not contribute to I_{CC}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The I and Y inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics Over Operating Range

Parameters	Description	Conditions	PI3B3257 Com.		Units
			Min.	Max.	
t_{IY}	Propagation Delay In to Yn[1,2]	C_L = 50pF R_L = 500Ω		0.25	ns
t_{SY}	Bus Select Time, Sn to Yn		1	4.5	
t_{PZH} t_{PZL}	Bus Enable Time, E to Yn		1	4.5	
t_{PHZ} t_{PLZ}	Bus Disable Time, E to Yn		1	4.8	

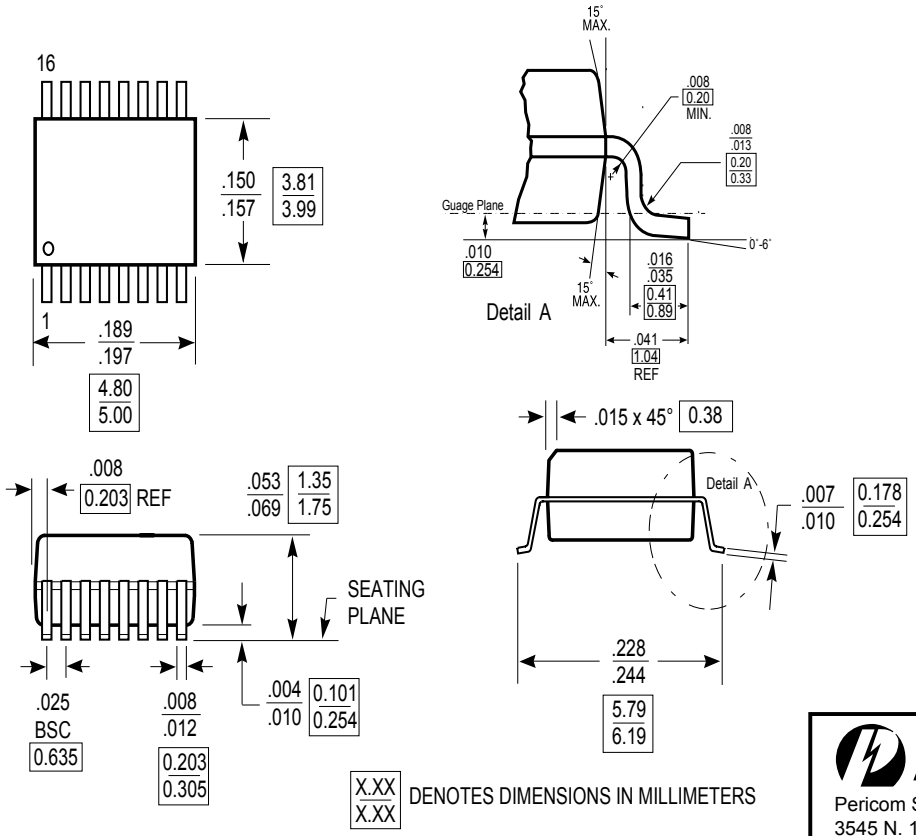
Notes:
1. This parameter is guaranteed but not tested on Propagation Delays.
2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The switch's time constant alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Packaging Mechanical: 16-Pin QSOP (Q)

DOCUMENT CONTROL NO.
PD - 1201

REVISION: G

DATE: 11/07/07



Note:
1) Controlling dimensions in inches.
2) Ref: JEDEC MO-137B/AB.
3) Dimensions do not include mold flash, protrusions or gate burrs

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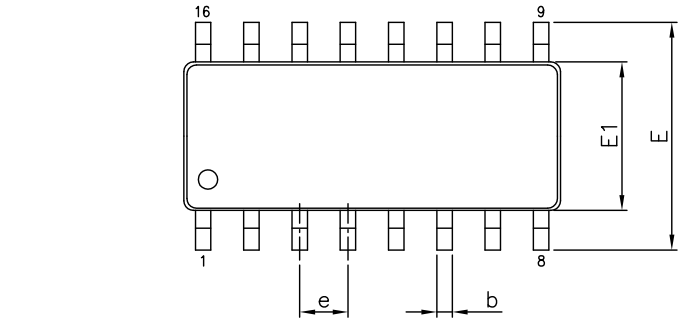
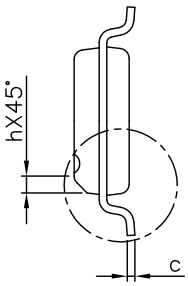
DESCRIPTION: 16-Pin 150-Mil Wide QSOP

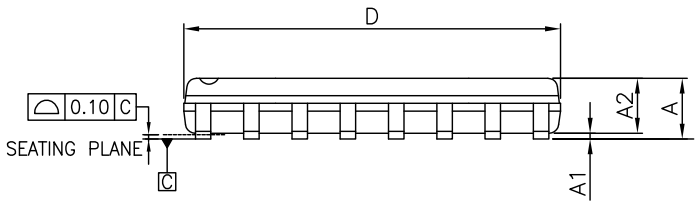
PACKAGE CODE: Q

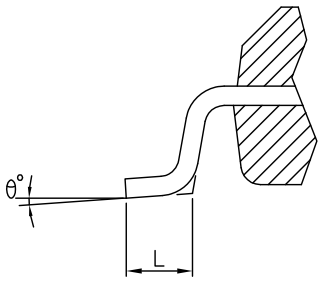
Packaging Mechanical: 16-Pin SOIC (W)





SYMBOLS	MIN.	NOM.	MAX.
A	–	–	1.75
A1	0.10	–	0.25
A2	1.25	–	–
b	0.31	–	0.51
c	0.10	–	0.25
D	9.80	9.90	10.0
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27 BSC		
L	0.40	–	1.27
h	0.25	–	0.50
θ°	0	–	8





NOTES:
1. ALL DIMENSIONS IN MILLIMETERS. ANGLES IN DEGREES.
2. JEDEC OUTLINE : MS–012 AC
3. DIMENSIONS DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

PERICOM Enabling Serial Connectivity

DATE: 06/15/12

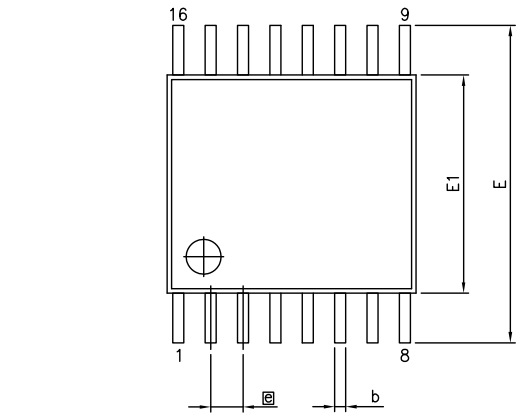
DESCRIPTION: 16-Pin, 150mil Wide SOIC

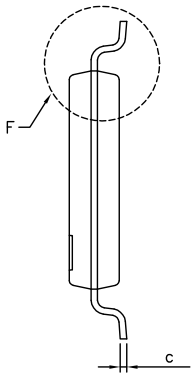
PACKAGE CODE: W

DOCUMENT CONTROL #: PD-1004 **REVISION: F**

2012-0398

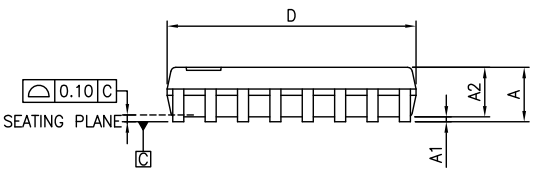
Packaging Mechanical: 16-Pin TSSOP (L)

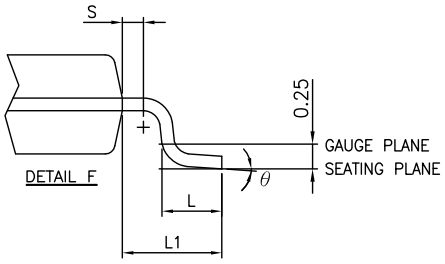




VARIATIONS (ALL DIMENSIONS SHOWN IN MM)

SYMBOLS	MIN.	NOM.	MAX.
A	–	–	1.20
A1	0.05	–	0.15
A2	0.80	–	1.05
b	0.19	–	0.30
c	0.09	–	0.20
D	4.90	5.00	5.10
E1	4.30	4.40	4.50
E	6.40 BSC		
e	0.65 BSC		
L1	1.00 REF		
L	0.45	0.60	0.75
S	0.20	–	–
θ	0°	–	8°





Notes:
1. Refer JEDEC MO-153F/AB
2. Controlling dimensions in millimeters
3. Package outline exclusive of mold flash and metal burr

PERICOM Enabling Serial Connectivity

DATE: 05/03/12

DESCRIPTION: 16-Pin, 173mil Wide TSSOP

PACKAGE CODE: L

DOCUMENT CONTROL #: PD-1310 **REVISION: F**

12-0372

Applications Information

Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a + 3.3V supply, IN may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

Power-Supply Sequencing and Hot-Plug Information

Proper power-supply sequencing is recommended for all CMOS devices. Always apply V_{CC} and GND before applying signals to input/output or control pins.

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Ordering Information

Ordering Code	Packaging Code	Package Description
PI3B3257QE	Q	Pb-Free & Green, 16-pin 150 mil wide plastic QSOP
PI3B3257WE	W	Pb-Free & Green, 16-pin 150-mil wide plastic SOIC
PI3B3257LE	L	Pb-Free & Green, 16-pin 173 mil wide plastic TSSOP

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel