Applications

- PC Multimedia
- Tablets
- Security

- Entertainment
- Cellular and Mobile Phones
- Games

Product Features

- automatic black level calibration (ABLC) support 2x2 binning
- programmable controls for frame rate, mirror and flip, cropping and windowing
- image quality controls: lens correction and defective pixel canceling
- supports output formats: 8/10/12-bit RAW RGB (MIPI/LVDS)
- supports horizontal and vertical sub-sampling
- supports images sizes: 1280x800, 640x400, 320x200, and 160x100
- fast mode switching

- - standard serial SCCB interface
 - two-lane MIPI/LVDS serial output interface
 - embedded 256 bits one-time programmable (OTP) memory for part identification, etc.
 - on-chip phase lock loop (PLL)
 - programmable I/O drive capability
 - built-in 1.5V regulator for core
 - support alternate frame HDR/line HDR

OV09714-A49A (color, lead-free, 49-pin CSP3)

Product Specifications

- active array size: 1296 × 812
- power supply:
 core: 1.5 VDC ±5%
 analog: 2.6 3.0V
 I/O: 1.7 3.0V
- power requirements:
- active: 95 mA standby: 30 μA xshutdown: 5 μA
- temperature range:
 operating: -30°C to 85°C junction
- temperature - stable image: 0°C to 50°C junction temperature
- output formats: 12-bit RGB RAW
- lens size: 1/4"
- lens chief ray angle: 28.7° non-linear
- input clock frequency: 6 27 MHz

max S/N ratio: 39 dB ■ dynamic range: 73 dB @ 8x gain

0V9714

- maximum image transfer rate:
 1280x800: 60 fps -640x400:120 fps - 320x200: 240 fps
- sensitivity: 3300 mV/lux-sec
- scan mode: progressive
- maximum exposure interval: 800 x t_{ROW}
- **pixel size:** 3.0 μm x 3.0 μm
- dark current: 2.3 mV/s @ 50°C junction temperature
- image area: 3936 µm x 2460 µm
- package dimensions: 6110 μm x 4930 μm





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Functional Block Diagram