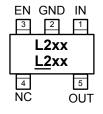
# **Ordering Information**

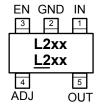
Part Number		Marking Codes			Junction		
Standard	Pb-Free	Standard	Pb-Free*	Voltage**	Temp. Range	Package	
MIC5235-1.5BM5	MIC5235-1.5YM5	L215	<u>L2</u> 15	1.5V	–40° to +125°C	5-Pin SOT-23	
MIC5235-1.8BM5	MIC5235-1.8YM5	L218	<u>L2</u> 18	1.8V	–40° to +125°C	5-Pin SOT-23	
MIC5235-2.5BM5	MIC5235-2.5YM5	L225	<u>L2</u> 25	2.5V	–40° to +125°C	5-Pin SOT-23	
MIC5235-2.7BM5	MIC5235-2.7YM5	L227	<u>L2</u> 27	2.7V	–40° to +125°C	5-Pin SOT-23	
MIC5235-3.0BM5	MIC5235-3.0YM5	L230	<u>L2</u> 30	3.0V	–40° to +125°C	5-Pin SOT-23	
MIC5235-3.3BM5	MIC5235-3.3YM5	L233	<u>L2</u> 33	3.3V	–40° to +125°C	5-Pin SOT-23	
MIC5235-5.0BM5	MIC5235-5.0YM5	L250	<u>L2</u> 50	5.0V	–40° to +125°C	5-Pin SOT-23	
MIC5235BM5	MIC5235YM5	L2AA	<u>L2</u> AA	Adj.	–40° to +125°C	5-Pin SOT-23	

<sup>\*</sup> Under bar symbol (\_) may not be to scale.

# **Pin Configuration**







SOT-23-5 (Adjustable)

# **Pin Description**

Pin Number	Pin Name	Pin Function
1	IN	Supply Input.
2	GND	Ground.
3	EN	Enable (Input): Logic low = shutdown; logic high = enable.
4	NC (fixed)	No Connect.
	ADJ (adj.)	Adjust (Input): Feedback input. Connect to resistive voltage-divider network.
5	OUT	Regulator Output.

<sup>\*\*</sup> Contact factory regarding availability for voltages not listed.

# Absolute Maximum Ratings<sup>(1)</sup>

Input Supply Voltage	20V to 38V
Enable Input Voltage	0.3V to 38V
Power Dissipation	Internally Limited
Junction Temperature	40°C to +125°C
Storage Temperature ESD Rating <sup>(3)</sup>	65°C to +150°C
ESD Rating <sup>(3)</sup>	

# Operating Ratings<sup>(2)</sup>

Input Supply Voltage	2.3V to 24V
Enable Input Voltage	0V to 24V
Junction Thermal	
Package Thermal Resistance	
SOT-23-5 (θ <sub>JA</sub> )	235°C/W

# **Electrical Characteristics**(4)

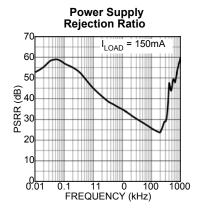
 $T_{A} = 25^{\circ}\text{C with V}_{IN} = V_{OUT} + 1\text{V}; \ I_{OUT} = 100\mu\text{A}, \ \textbf{Bold} \ \ \text{values indicate} \ -40^{\circ}\text{C} < T_{J} < +125^{\circ}\text{C}; \ \ \text{unless otherwise specified}.$ 

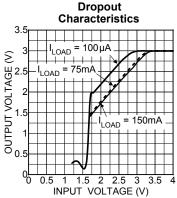
Parameter	Condition	Min	Тур	Max	Units
Output Voltage Accuracy	Variation from nominal V <sub>OUT</sub>	-1.0 <b>-2.0</b>		+1.0 <b>+2.0</b>	% %
Line Regulation	V <sub>IN</sub> = V <sub>OUT</sub> + 1V to 24V		0.04		%
Load Regulation	Load = 100µA to 150mA		0.25	1	%
Dropout Voltage	$I_{OUT} = 100 \mu A$ $I_{OUT} = 50 m A$ $I_{OUT} = 100 m A$ $I_{OUT} = 150 m A$		50 230 270 310	300 <b>400</b> 400 <b>450</b> 450	mV mV mV mV
	1001 1001111111		010	500	mV
Reference Voltage		1.22	1.24	1.25	V
Ground Current	I <sub>OUT</sub> = 100μA I <sub>OUT</sub> = 50mA I <sub>OUT</sub> = 100mA I <sub>OUT</sub> = 150mA		18 0.35 1 2	30 <b>35</b> 0.7 2 4	μΑ μΑ mA mA mA
Ground Current in Shutdown	V <sub>EN</sub> ≤ 0.6V; V <sub>IN</sub> = 24V		0.1	1	μA
Short Circuit Current	V <sub>OUT</sub> = 0V		350	500	mA
Output Leakage, Reverse Polarity Input	Load = $500\Omega$ ; $V_{IN} = -15V$		-0.1		μA
Enable Input		<u>.</u>			
Input Low Voltage	Regulator OFF			0.6	V
Input High Voltage	Regulator ON	2.0			V
Enable Input Current	$V_{EN}$ = 0.6V; Regulator OFF $V_{EN}$ = 2.0V; Regulator ON $V_{EN}$ = 24V; Regulator ON	-1.0	0.01 0.1 0.5	1.0 1.0 2.5	μΑ μΑ μΑ

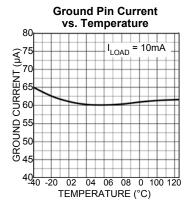
#### Notes:

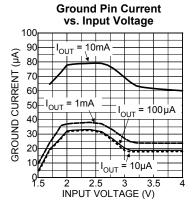
- 1. Exceeding the absolute maximum rating may damage the device.
- 2. The device is not guaranteed to function outside its operating rating.
- 3. Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5k in series with 100pF.
- 4. Specification for packaged product only.

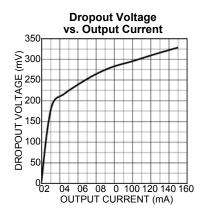
### **Typical Characteristics**

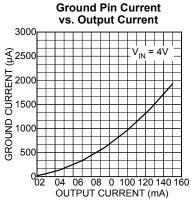


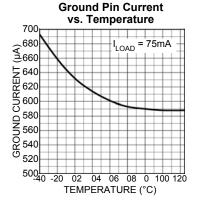


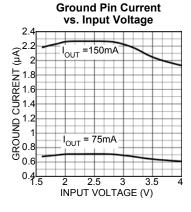


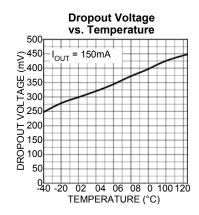


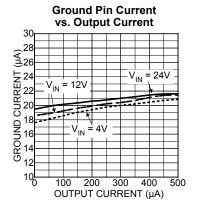


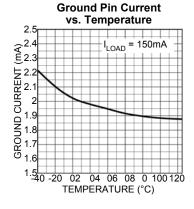


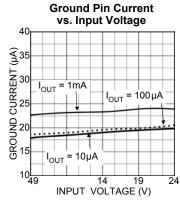




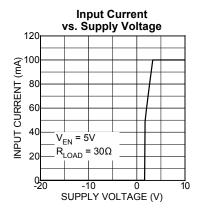


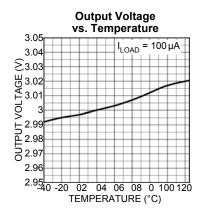


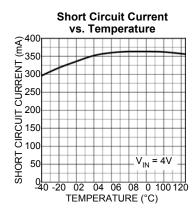


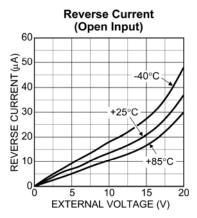


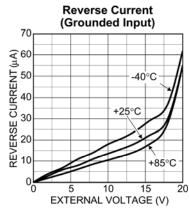
### **Typical Characteristics (continued)**

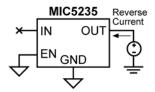


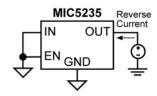


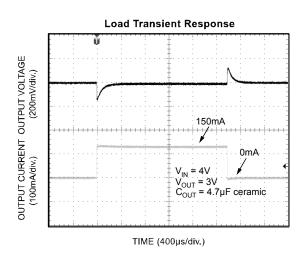




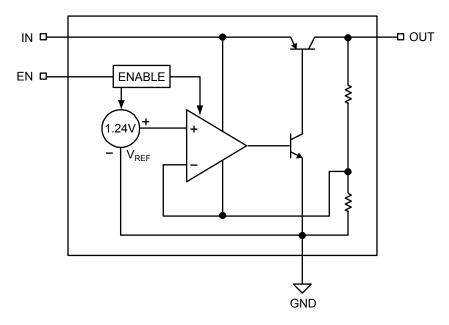




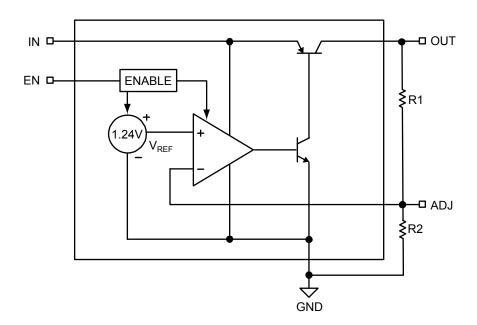




# **Functional Diagram**



**Block Diagram – Fixed Output Voltage** 



Block Diagram - Adjustable Output Voltage

### **Application Information**

#### Enable/Shutdown

The MIC5235 comes with an active-high enable pin that allows the regulator to be disabled. Forcing the enable pin low disables the regulator and sends it into a "zero" off-mode-current state. In this state, current consumed by the regulator goes nearly to zero. Forcing the enable pin high enables the output voltage.

#### **Input Capacitor**

The MIC5235 has high input voltage capability up to 24V. The input capacitor must be rated to sustain voltages that may be used on the input. An input capacitor may be required when the device is not near the source power supply or when supplied by a battery. Small, surface mount, ceramic capacitors can be used for bypassing. Larger values may be required if the source supply has high ripple.

#### **Output Capacitor**

The MIC5235 requires an output capacitor for stability. The design requires  $2.2\mu F$  or greater on the output to maintain stability. The design is optimized for use with low-ESR ceramic chip capacitors. High ESR capacitors may cause high frequency oscillation. The maximum recommended ESR is  $3\Omega.$  The output capacitor can be increased without limit. Larger valued capacitors help to improve transient response.

X7R/X5R dielectric-type ceramic capacitors recommended because of their temperature performance. X7R-type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60% respectively over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric, the value must be much higher than a X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

#### **No-Load Stability**

The MIC5235 will remain stable and in regulation with no load unlike many other voltage regulators. This is especially important in CMOS RAM keep-alive applications.

#### **Thermal Considerations**

The MIC5235 is designed to provide 150mA of continuous current in a very small package. Maximum power dissipation can be calculated based on the output current and the voltage drop across the part. To determine the maximum power dissipation of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(MAX)} = \left(\frac{T_{J(MAX)} - T_{A}}{\theta_{JA}}\right)$$

 $T_{J(MAX)}$  is the maximum junction temperature of the die, 125°C, and  $T_A$  is the ambient operating temperature.  $\theta_{JA}$  is layout dependent; Table 1 shows examples of the junction-to-ambient thermal resistance for the MIC5235.

Package	θ <sub>JA</sub> Recommended Minimum Footprint
SOT-23-5	235°C

Table 1. SOT-23-5 Thermal Resistance

The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT})I_{OUT} + V_{IN}I_{GND}$$

Substituting  $P_{D(MAX)}$  for  $P_D$  and solving for the operating conditions that are critical to the application will give the maximum operating conditions for the regulator circuit. For example, when operating the MIC5235-3.0BM5 at 50°C with a minimum footprint layout, the maximum input voltage for a set output current can be determined as follows:

$$P_{D(MAX)} = \left(\frac{125^{\circ}C - 50^{\circ}C}{235^{\circ}C/W}\right)$$

$$P_{D(MAX)} = 319mW$$

The junction-to-ambient ( $\theta_{JA}$ ) thermal resistance for the minimum footprint is 235°C/W, from Table 1. It is important that the maximum power dissipation not be exceeded to ensure proper operation. Since the MIC5235 was designed to operate with high input voltages, careful consideration must be given so as not to overheat the device. With very high input-to-output voltage differentials, the output current is limited by the total power dissipation. Total power dissipation is calculated using the following equation:

$$P_D = (V_{IN} - V_{OUT})I_{OUT} + V_{IN} \times I_{GND}$$

Due to the potential for input voltages up to 24V, ground current must be taken into consideration. If we know the maximum load current, we can solve for the maximum input voltage using the maximum power dissipation calculated for a 50°C ambient, 319mV.

$$P_{D(MAX)} = (V_{IN} - V_{OUT})I_{OUT} + V_{IN} x I_{GND}$$
  
319mW =  $(V_{IN} - 3V)150mA + V_{IN} x 2.8mA$ 

Ground pin current is estimated using the typical characteristics of the device.

$$769\text{mW} = V_{IN} (152.8\text{mA})$$
  
 $V_{IN} = 5.03\text{V}$ 

For higher current outputs only a lower input voltage will work for higher ambient temperatures.

Assuming a lower output current of 20mA, the maximum input voltage can be recalculated:

$$319\text{mW} = (V_{IN} - 3V)20\text{mA} + V_{IN} \times 0.2\text{mA}$$
  
 $379\text{mW} = V_{IN} \times 20.2\text{mA}$   
 $V_{IN} = 18.8V$ 

Maximum input voltage for a 20mA load current at 50°C ambient temperature is 18.8V, utilizing virtually the entire operating voltage range of the device.

#### **Adjustable Regulator Application**

The MIC5235BM5 can be adjusted from 1.24V to 20V by using two external resistors (Figure 1). The resistors set the output voltage based on the following equation:

$$V_{OUT} = V_{REF} \left( 1 + \left( \frac{R_1}{R_2} \right) \right)$$

Where  $V_{REF} = 1.24V$ .

Feedback resistor R2 should be no larger than  $300k\Omega$ .

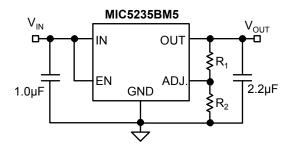
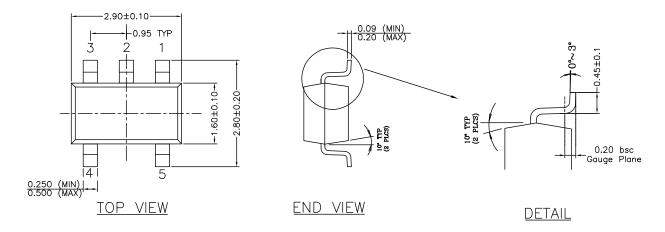
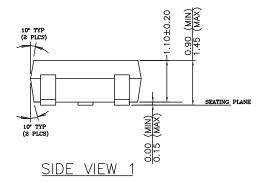


Figure 1. Adjustable Voltage Application

### **Package Information**





- 1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & BURR. 2. PACKAGE OUTLINE INCLUSIVE OF SOLER PLATING.

- 3. DIMENSION AND TOLERANCE PER ANSI Y14.5M, 1982. 4. FOOT LENGTH MEASUREMENT BASED ON GAUGE PLANE METHOD.
- 5. DIE FACES UP FOR MOLD, AND FACES DOWN FOR TRIM/FORM.

SOT-23-5 (M5)

#### MICREL, INC. 2180 FORTUNE DRIVE SAN JOSE, CA 95131 USA

TEL +1 (408) 944-0800 FAX +1 (408) 474-1000 WEB http://www.micrel.com

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